

Third International Workshop on **FPGAs for Software Programmers (FSP 2016)**

August 29, 2016, Lausanne, Switzerland

co-located with

International Conference on Field Programmable Logic and Applications (FPL)

Preface

Program

09:00 - 09:05	Workshop Opening
09:05 - 10:00	Keynote 1: "A Comprehensive Approach to Software-Defined FPGA Computing" <i>Gael Paul, PLDA</i>
10:00 - 10:30	"Automated Generation of Reconfigurable Systems-on-Chip by Interactive Code Transformations for High-Level Synthesis" <i>Silvano Brugnoli, Thomas Corbat, Peter Sommerlad, Toni Suter, Jens Korinth, David de la Chevallerie, Andreas Koch</i>
10:30 - 11:00	Coffee Break
11:00 - 11:30	"A Compute Model for Generating High Performance Computing SoCs on Hybrid Systems with FPGAs" <i>Felix Friedrich, Oleksii Morozov, Patrick Hunziker</i>
11:30 - 12:15	Keynote 2: "An Open Ecosystem for Software Programmers to Compute on FPGAs" <i>Paul Chow, University of Toronto</i>
12:30 - 14:15	Lunch
14:15 - 15:00	Embedded Tutorial: "Enabling Software Engineers to Program Heterogeneous, Reconfigurable SoCs" <i>Patrick Lysaght, Xilinx Research Labs (Not available on CD)</i>
15:00 - 15:30	"Transparent Live Code Offloading on FPGA" <i>Roberto Rigamonti, Baptiste Delporte, Anthony Convers, Alberto Dassatti</i>
15:30 - 16:00	Coffee Break
16:00 - 16:15	"An interactive environment for mapping computational structures to FPGAs" <i>Rinse Wester, Robert de Groote (Short Paper)</i>
16:15 - 16:45	"Automated Inference of SoC Configuration through Firmware Source Code Analysis" <i>Kris Heid, Ramon Wirsch, Christian Hochberger</i>
16:45 - 17:15	"Building hardware from C# models" <i>Kenneth Skovhede, Brian Vinter</i>
17:15 - 17:20	Workshop Closing