

WORKSHOPS PROGRAM

13th Workshop on Dependability and Fault Tolerance and 1st Workshop on Computer Architectures in Space

- 3 *Luca Sterpone, Sarah Azimi*
Radiation-induced SET on Flash-based FPGAs: Analysis and Filtering methods
 - 9 *Monica Alderighi, Sergio D'Angelo, Francesco Casini, Giorgio Sorrenti, David Merodio Codinachs, Sara Davin*
The FLIPPER Fault Injection Platform: Experiences and Knowledge from a Ten-year Project
 - 17 *Sebastian Plamauer, Martin Langer*
Evaluation of MicroPython as Application Layer Programming Language on CubeSats
 - 26 *Bernhard Seifert, Alexander Reissner, Thomas Hörbe*
Computer Architecture of the PPU for integrated FEEP Propulsion System
 - 32 *Alejandro David Velasco, Bartolomeo Montrucchio, Maurizio Rebaudengo*
Hardening Approach for the Scheduler's Kernel Data Structures
-

5th International Workshop on Self-Optimisation in Autonomic and Organic Computing Systems

- 39 *Stefan Rudolph, Rainer Hihn, Sven Tomforde, Jörg Hähner*
Towards Discovering Delayed Mutual Influences in Organic Computing Systems
 - 47 *Jan Kantert, Sven Tomforde, Ada Diaconescu, Christian Müller-Schloer*
Incentive-oriented Task Assignment in Holonic Organic Systems
 - 55 *Sarah Edenhofer, Youssef Madkour, Anthony Stein, Christopher Stifter, Jörg Hähner*
Bottom-Up Norm Creation in Open Distributed Computing Grids by Means of eXtended Classifier Systems
 - 63 *Andreas Lund, Mathias Pacher, Uwe Brinkschulte*
Task-Allocation in a hierarchical network topology by means of an organic middleware
 - 71 *Oliver Meisch, Gerben Peet, Stefan Rudolph, Jörg Hähner, Sebastian von Mammen*
Pick Again: Self-Adaptive Warehouse Commissioning
 - 78 *Christian Gruhl, Frank Beer, Henner Heck, Bernhard Sick, Ulrich Bühler, Arno Wacker, Sven Tomforde*
A Concept for Intelligent Collaborative Network Intrusion Detection
 - 86 *Sven Tomforde, Christian Gruhl, Jörg Hähner*
A Concept for Self-adapting and Self-learning Traffic Offloading in Cellular Networks
-

2nd FORMUS³IC Workshop

- 96 *Sebastian Rachuj, Christian Hartmann, Dietmar Fey*
Evaluation of a Processor Simulator Exemplified by a Radar Processing Algorithm
- 101 *Christoph Hartmann, Ralph Mader, Lothar Michel, Christos Ebert, Ulrich Margull*
Massive Parallelization of Real-World Automotive Real-Time Software by GPGPU
- 109 *Tobias Langer, Lukas Osinski, Jürgen Mottok*
A Survey of Parallel Hard-Real Time Scheduling On Task Models and Scheduling Approaches

- 117 *Lukas Osinski, Tobias Langer, Jürgen Mottok*
A survey of fault tolerance approaches on different architecture levels
- 126 *André Göbel, Ovidiu Tripon*
Performance and Freedom From Interference - a contradiction in embedded automotive multi-core applications?
- 135 *Christian Hartmann, Christian Widerspick, Dietmar Fey*
A Methodology to Estimate the Energy Consumption and Processing Time for Image Processing Algorithms in Advanced Driver Assistance Systems
- 141 *Christian Widerspick, Christian Hartmann, Dietmar Fey*
Estimation of Time Behavior of Selected Autonomous Driving Algorithms using GPGPU-Sim
- 146 *Christian Zoubek, Peter Trommler*
Overview of worst case execution time analysis in single- and multicore environments

- 151 **List of Authors**