

Contents

Technical Sessions

1. Beyond Moore implementations: Memristors and Oscillators

Chairman: Valeri Mladenov

- 1.1 Two-dimensional memristive CNN for sequence recognition 9
Stanislaw Jankowski, Zbigniew Szymański, Zbigniew Wawrzyniak, Warsaw University of Technology, Warsaw, Poland
- 1.2 Theory of CNNs with hafnium oxide RRAMs 12
Alon Ascoli, Ronald Tetzlaff, TU Dresden, Dresden, Germany; Daniele Ielmini, Politecnico di Milano, Milan, Italy; Leon Ong Chua, University of California Berkeley, California, USA
- 1.3 Synthesis and Analysis of a Memristor-Based Artificial Neuron 15
Valeri Mladenov, Technical University of Sofia, Sofia, Bulgaria
- 1.4 Game of Life in Memristor Cellular Automata Grid 19
Rafailia-Eleni Karamani, Democritus University of Thrace, Xanthi, Greece;
Iosif-Angelos Fyrigos, Democritus University of Thrace, Xanthi, Greece;
Vasileios Ntinasy, Universitat Politècnica de Catalunya, Barcelona, Spain;
Ioannis Vourkasz, Universidad Técnica Federico Santa María, Valparaíso, Chile;
Georgios Ch. Sirakoulis, Democritus University of Thrace, Xanthi, Greece
- 1.5 Tunable Chaos in Memristor Circuits for Pattern Recognition Tasks 23
Francesco Marrone, Fernando Corinto, Politecnico di Torino, Turin, Italy
- 1.6 Ring Oscillators to Model Artificial Neural Networks 27
Linda Gong, University of Notre Dame, Notre Dame, IN, USA
- 1.7 Hierarchical Modeling of Nano-Oscillator Systems 30
Zachary Hull, Donald Chiarulli, University of Pittsburgh, Pittsburgh, USA

2. Medical Applications

Chairman: Ákos Zarandy

- 2.1 Mammogram Classification with Local Phase Quantization Features 34
Ioan Buciu, Cristian Grava, Laviniu Tepelea, Alexandru Gacsàdi, University of Oradea, Oradea, Romania
- 2.2 Complex Spatio-Temporal Patterns in Red Blood Cells Flows 38
Fabiana Cairone, Maide Bucolo, University of Catania, Catania, Italy
- 2.3 Remote camera based measurement of human vital signs 42
Dániel Terbe, Ákos Zarandy, MTA SZTAKI, Budapest, Hungary

2.4 Automatic skin lesion analysis using relatively small learning set	47
Orsolya Heri, Hungarian Academy of Sciences, Hungary;	
Antal Hiba, Akos Zarandy, Hungarian Academy of Sciences, MTA SZTAKI, Budapest, Hungary	
2.5 A New Approach for Motion Estimation and Correction of Thermographic Images in Brain Surgery	51
Yahya Moshaei-Nezhad, Jens Müller, Ronald Tetzlaff, Nico Hoffmann, Technische Universität Dresden, Dresden, Germany	

3. Cameras, Architectures, and VLSI Implementation

Chairman: Ricardo Carmona

3.1 A Smart Camera Architecture with Keypoint Description and Hybrid Processor Population	55
Selman Ergünay, Yusuf Leblebici, Ecole Polytechnique Fédérale de Lausanne (EPFL), Lausanne, Switzerland	
3.2 Two-Layer Cellular Neural Networks with Layer of Delay Output	59
Takahisa Ando, Yoko Uwate, Yoshifumi Nishio, Tokushima University, Tokushima, Japan	
3.3 Asynchronous Object Center Extraction for Pixel Detectors	63
Ari Paasio, University of Turku, Turku, Finland	
3.4 1D Cellular Automata for Pulse Width Modulated Compressive Sampling CMOS Image Sensors	66
Marco Trevisi, Ricardo Carmona-Galán, Ángel Rodríguez-Vázquez, CSIC-Universidad de Sevilla, Spain	
3.5 Approximating Binary Object Skeletonization with Pixel-Level Asynchronous Propagation ..	70
Ari Paasio, Jonne K. Poikonen, University of Turku, Turku, Finland	

4. Applications

Chairman: Antal Hiba

4.1 Color Filter Array Interpolation Using Cellular Neural Networks Considering Self-Congruence	74
Taishi Iriyama, Masatoshi Sato, Tsuyoshi Otake, Tamagawa University, Tokyo, Japan;	
Hisashi Aomori, Chukyo University, Aichi, Japan; Mamoru Tanaka, Sophia University, Tokyo, Japan	
4.2 Cellular Automaton Based Random Noise Generator with Post-Processing for DT-CNN Annealing	78
Tomohiro Fujita, Masami Nakayama, Takeshi Kumaki, Takeshi Ogura, Ritsumeikan University, Shiga, Japan	
4.3 Camera-based In-time Detection of Intruder Aircraft	82
Peter Bauer, Akos Zarandy, Antal Hiba, Jozsef Bokor, MTA SZTAKI, Budapest, Hungary	
4.4 Runway detection for UAV landing system	86
Antal Hibay, MTA SZTAKI, Budapest, Hungary & Pázmány Péter Catholic University, Budapest, Hungary; Tamas Zsedrovitsy, Pázmány Péter Catholic University, Budapest, Hungary;	
Orsolya Heri, Hungarian Academy of Sciences, Hungary; Akos Zarandy, MTA SZTAKI, Budapest, Hungary & Pázmány Péter Catholic University, Budapest, Hungary	

4.5 Performance Evaluation of a Track to Track Sensor Fusion Algorithm	90
Lóránt Kovács, Pázmány Péter Catholic University, Budapest, Hungary;	
László Lindenmaier, Budapest University of Technology and Economics, Hungary;	
Huba Németh, Budapest University of Technology and Economics, Hungary;	
Viktor Tihanyi, Budapest University of Technology and Economics, Hungary;	
Ákos Zarányi, Pázmány Péter Catholic University, Budapest, Hungary & MTA SZTAKI, Budapest, Hungary	

5. FPGA Implementation

Chairman: Mustak Yalcin

5.1 An abstraction for local computations on structured meshes and its extension to handling multiple materials	92
D. Becker, I. Z. Reguly, Pazmany Peter Catholic University, Budapest, Hungary;	
G. R. Mudalige, University of Warwick, Coventry, UK	
5.2 Real-Time Video Frame Differentiator Based on DDR3 SDRAM Memory Interface	96
Dogancan Davutoglu, Department of EEE, Istanbul Kultur University, Istanbul, Turkey;	
Nerhun Yildiz, Department of Media Engineering, ARM Limited, Leicester, UK;	
Vedat Tavsanoglu, Isik University, Istanbul, Turkey; Umut Engin Ayten, Yildiz Technical University, Istanbul, Turkey	
5.3 An Efficient Multi-Level Fast Multipole Method Implementation on FPGA	99
András Kiss, Zoltán Nagy, Pázmány Péter Catholic University, Budapest, Hungary;	
Levente Márk Sántha, Pázmány Péter Catholic University, Budapest, Hungary & MTA SZTAKI, Budapest, Hungary; György Csaba, Pázmány Péter Catholic University, Budapest, Hungary	
5.4 A New Architecture for Emulating CNN with Template Learning on FPGA	103
Erdem Köse, Gebze Technical University, Gebze-Kocaeli, Turkey;	
Müstak E. Yalcin, Istanbul Technical University, Istanbul, Turkey	
5.5 Comparison of numerical integration methods for digital hardware implementations	107
Jens Müller, Ronald Tetzlaff, Technische Universität Dresden, Dresden, Germany	

6. Demo Session

Chairman: Zoltan Nagy

6.1 Demo: Real-Time Video Frame Differentiator Based on External Memory Interface	111
Dogancan Davutoglu, Istanbul Kultur University, Istanbul, Turkey;	
Nerhun Yildiz, Department of Media Engineering, ARM Limited, Leicester, UK;	
Vedat Tavsanoglu, Isik University, Istanbul, Turkey;	
Umut Engin Ayten, Yildiz Technical University, Istanbul, Turkey	

7. Network Theory and Learning

Chairman: Paolo Arena

7.1 Controlling synchronization of a group of network nodes	112
Lucia Valentina Gambuzza, Mattia Frasca, Università degli Studi di Catania, Catania, Italy;	
Vito Latora, Queen Mary University of London, London, UK & University of Catania and INFN, Italy	

7.2 Dynamics of a Chua-Yang ring network in 8D	114
Miklós Koller, Marcell Simkó, Barnabas M. Garay, Pázmány Péter Catholic University, Budapest, Hungary	
7.3 Role of feedback and local coupling in CNNs for locomotion control of a quadruped robot ..	118
Paolo Arena, Andrea Bonanzinga, Luca Patané, DIEEI, Universitá degli studi di Catania, Catania, Italy	
7.4 Applying the Standard Non-linearity of Cellular Neural Networks in Convolutional Networks	122
András Horváth, Domonkos Ábrahám, Pázmány Péter Catholic University, Budapest, Hungary	
7.5 Application of the Nonlinear Wave Metric for Image Segmentation in Neural Networks ...	125
Jalal Al-Afandi, András Horváth, Pázmány Péter Catholic University, Budapest, Hungary	