

## Preface

Like all previous editions, the Sixths International Workshop on FPGAs for Software Programmers (FSP) is co-located with the International Conference on Field-Programmable Logic and Applications (FPL); and its 2019 edition is therefore located at the Barcelona Supercomputer Center (BSC) in Spain. As in previous years, FSP continues to publish papers in IEEE Xplore-indexed proceedings for better visibility. In order to remove financial barriers to attend FSP to its minimum, we aim at low registration fees and we kindly thank this year's sponsor Silexica GmbH (Germany) to cover all proceedings cost.

With FPGAs becoming mainstream in many datacenters and the need to get a very wide range of applications implemented with better productivity than ever before, the core FSP theme is of rising importance. And like in all previous issues of FSP, the key focus of FSP 2019 remains on looking at key aspects of using FPGA-based reconfigurable computing from a **software developer's** perspective. With this, FSP is keen to maintain its **workshop** nature: It remains a venue for presenting not just mature research, but also exposing promising work-in-progress or prototype reports to a wider community for inspiring scientific discourse.

The FSP chairs thank the Program Committee for their fantastic and thorough input and valuable discussions allowing to finally select six submissions (66 %) to be presented as full papers. These cover a wide range of topics, ranging from HLS acceleration and optimizing HLS generated circuits to heterogeneous systems and runtime systems to manage such heterogeneous systems. To open this program, a first keynote by Juan Eusse (Silexica) covers HLS for heterogeneous systems from an industry perspective. In a second keynote, Christophe Bobda (University of Florida, US) dives into FPGA virtualization aspects for large scale FPGA adaptation as required by software communities. As new elements at FSP, we have two invited contributions addressing the needs of software engineers: Tobias Kentner (Paderborn University), is giving a tutorial on OpenCL design patterns and design for portability and Luciano Lavango (Politecnico di Torino, Italy) presents how to use datacenter FPGA boards.

Finally we thank all authors and attendees for contributing to FSP 2019.

### FSP 2019 Co-Chairs

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