

DVCon Europe – Research track

In 2013, Accellera started the preparation of the first DVCon Europe, held in October 2014 in Munich, Germany. At that time, there was only one DVCon, a well-established and recognized conference in Silicon Valley. Today, the “Design & Verification Conference & Exhibition Europe”, known as DVCon Europe is a premier conference on the application of languages, tools, methodologies and standards for the design and verification of electronic systems and integrated circuits. It features a significant industrial participation and exhibition of EDA vendors, and leading research contributions.

DVCon Europe 2023, celebrating its 10th anniversary, is strengthening the connection to research by introducing a “Research track”, which is fully embedded into the technical program. All research papers were carefully reviewed and selected following scientific standards (double-blinded, 4 reviews by experts in the field). The program committee has selected 12 excellent submissions that were accepted for both presentation at the conference and publication in these proceedings.

The research presentations are fully integrated into the conference program to drive transfer among research and industrial application. For the proceedings, we grouped the papers selected for the research track into the following sections:

Analog/Mixed-Signal Design; where the challenge is to create robust and reliable systems from components that subject to aging and process variations, and to verify them by simulation with reasonable simulation times.

Multi-Processor and Distributed Architectures, where the challenge is to create a system partitioning or mapping based on rather uncertain information.

Evaluation and Verification; in general, methods for verification and evaluation are a challenge in presence of growing complexity.

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