

TECHNICAL SPECIFICATION



**Nanomanufacturing – Key control characteristics –
Part 6-16: Two-dimensional materials – Carrier concentration: Field effect
transistor method**

INTERNATIONAL
ELECTROTECHNICAL
COMMISSION

ICS 07.030; 07.120

ISBN 978-2-8322-6054-8

Warning! Make sure that you obtained this publication from an authorized distributor.

CONTENTS

FOREWORD.....	4
INTRODUCTION.....	6
1 Scope.....	7
2 Normative references	7
3 Terms and definitions	7
3.1 General terms	8
3.2 Key control characteristics measured in accordance with this document	8
3.3 Terms related to the measurement method	9
4 General	9
4.1 Measurement principle	9
4.2 Sample preparation method	9
4.2.1 Sample preparation	9
4.2.2 Fabrication of FET	9
4.3 Description of measurement equipment	10
4.4 Ambient conditions during measurement	11
5 Measurement procedure	12
5.1 Calibration of measurement equipment	12
5.2 Detailed protocol of the measurement procedure	12
6 Data analysis and interpretation of results	12
6.1 General.....	12
6.2 When the minimum conductance neutral point is clear	12
6.3 When the minimum conductance neutral point is unclear	13
7 Results to be reported	13
7.1 Cover sheet	13
7.2 Product or sample identification	14
7.3 Measurement conditions	14
7.4 Measurement specific information.....	14
7.5 Measurement results.....	14
Annex A (informative) Graphene FET.....	15
A.1 Background.....	15
A.2 Test report	15
Annex B (informative) Graphene/hBN/MoS ₂ heterostructure memory FET	16
B.1 Background.....	16
B.2 Test report	18
Annex C (informative) MoTe ₂ FET	19
C.1 Background.....	19
C.2 Test report	20
Annex D (informative) WSe ₂ FET	21
D.1 Background.....	21
D.2 Test report	22
Bibliography.....	23

Figure 1 – Schematic of a back-gated graphene FET (inset: top view of the optical microscopic image)..... 10

Figure 2 – Experimental setup for measurements of electrical properties of FET device..... 11

Figure 3 – Voltage shift obtained from transfer curves upon plasma doping with various plasma treatments onto the graphene, using 300-nm-thick SiO ₂ back gate insulator.....	13
Figure 4 – Voltage shift obtained from transfer curves of MoS ₂ FET	13
Figure B.1 – Heterostructure FETs: (a) schematic view and circuit diagram of the fabricated device; (b) optical microscopic photograph of GBM FET; (c) optical microscopic photograph of MBG FET	16
Figure B.2 – Voltage shift obtained from transfer curves of two types of memory device upon charge injection.....	17
Figure C.1 – Optical microscopic image of MoTe ₂ FET and the thickness of 2D MoTe ₂ measured by AFM.....	19
Figure C.2 – Voltage shift observed from transfer curves measured by using 2D MoTe ₂ FET	20
Figure D.1 –WSe ₂ FET	21
Figure D.2 – Transfer curves of 2D WSe ₂ FET devices before and after doping with contacts (inset: output curves of devices before and after doping)	22
Table 1 – Specification of key control characteristics, 2D carrier concentration	12
Table A.1 – 2D carrier concentration measured from graphene-FET for different doping-inducing Ar plasma treatment times.....	15
Table B.1 – Carrier concentration derived from the electrical characteristics of GBM and MBG	18

INTERNATIONAL ELECTROTECHNICAL COMMISSION

**NANOMANUFACTURING –
KEY CONTROL CHARACTERISTICS –**

**Part 6-16: Two-dimensional materials –
Carrier concentration: Field effect transistor method**

FOREWORD

- 1) The International Electrotechnical Commission (IEC) is a worldwide organization for standardization comprising all national electrotechnical committees (IEC National Committees). The object of IEC is to promote international co-operation on all questions concerning standardization in the electrical and electronic fields. To this end and in addition to other activities, IEC publishes International Standards, Technical Specifications, Technical Reports, Publicly Available Specifications (PAS) and Guides (hereafter referred to as "IEC Publication(s)"). Their preparation is entrusted to technical committees; any IEC National Committee interested in the subject dealt with may participate in this preparatory work. International, governmental and non-governmental organizations liaising with the IEC also participate in this preparation. IEC collaborates closely with the International Organization for Standardization (ISO) in accordance with conditions determined by agreement between the two organizations.
- 2) The formal decisions or agreements of IEC on technical matters express, as nearly as possible, an international consensus of opinion on the relevant subjects since each technical committee has representation from all interested IEC National Committees.
- 3) IEC Publications have the form of recommendations for international use and are accepted by IEC National Committees in that sense. While all reasonable efforts are made to ensure that the technical content of IEC Publications is accurate, IEC cannot be held responsible for the way in which they are used or for any misinterpretation by any end user.
- 4) In order to promote international uniformity, IEC National Committees undertake to apply IEC Publications transparently to the maximum extent possible in their national and regional publications. Any divergence between any IEC Publication and the corresponding national or regional publication shall be clearly indicated in the latter.
- 5) IEC itself does not provide any attestation of conformity. Independent certification bodies provide conformity assessment services and, in some areas, access to IEC marks of conformity. IEC is not responsible for any services carried out by independent certification bodies.
- 6) All users should ensure that they have the latest edition of this publication.
- 7) No liability shall attach to IEC or its directors, employees, servants or agents including individual experts and members of its technical committees and IEC National Committees for any personal injury, property damage or other damage of any nature whatsoever, whether direct or indirect, or for costs (including legal fees) and expenses arising out of the publication, use of, or reliance upon, this IEC Publication or any other IEC Publications.
- 8) Attention is drawn to the Normative references cited in this publication. Use of the referenced publications is indispensable for the correct application of this publication.
- 9) Attention is drawn to the possibility that some of the elements of this IEC Publication may be the subject of patent rights. IEC shall not be held responsible for identifying any or all such patent rights.

IEC TS 62607-6-16 has been prepared by IEC technical committee 113: Nanotechnology for electrotechnical products and systems. It is a Technical Specification.

The text of this Technical Specification is based on the following documents:

Draft	Report on voting
113/679/DTS	113/698/RVDTS

Full information on the voting for its approval can be found in the report on voting indicated in the above table.

The language used for the development of this Technical Specification is English.

This document was drafted in accordance with ISO/IEC Directives, Part 2, and developed in accordance with ISO/IEC Directives, Part 1 and ISO/IEC Directives, IEC Supplement, available at www.iec.ch/members_experts/refdocs. The main document types developed by IEC are described in greater detail at www.iec.ch/publications.

A list of all parts in the IEC TS 62607 series, published under the general title *Nanomanufacturing – Key control characteristics*, can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under webstore.iec.ch in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

IMPORTANT – The "colour inside" logo on the cover page of this document indicates that it contains colours which are considered to be useful for the correct understanding of its contents. Users should therefore print this document using a colour printer.

INTRODUCTION

Atomically thin 2D materials are expected to be used for future electrical sub-systems or electronic device applications. For these applications, the materials need to be doped with dopants to generate carriers. In contrast to 3D bulk materials, carrier concentrations in 2D materials are difficult to measure directly due to their limited thickness.

- Different from conventional 3D bulk materials in which doping effect is induced from activation of substitutional dopant atoms, the doping effect in 2D materials is mostly induced by generation of free carriers, for example electrons by using plasma treatment, chemical treatment, etc.
- In the 3D bulk materials, carrier concentration can be obtained by measuring concentration of dopant atoms under the assumption that both concentrations are the same. Therefore, it is possible to measure the doping concentration in 3D bulk materials using secondary ion mass spectroscopy (SIMS), which measures the concentration of dopant atoms, and using I-V or C-V characterization, which measures the concentration of free charge carriers such as electrons and holes [1]¹.
- In contrast, in the 2D materials, carrier concentration needs to be measured for carriers such as electrons and holes which are induced from external means such as plasma treatment or chemical treatment.

For this reason, a standard method to determine the carrier concentration needs to be established for 2D materials.

¹ Numbers in square brackets refer to the Bibliography.

NANOMANUFACTURING – KEY CONTROL CHARACTERISTICS –

Part 6-16: Two-dimensional materials – Carrier concentration: Field effect transistor method

1 Scope

This part of IEC TS 62607 establishes a standardized method to determine the key control characteristic

- carrier concentration
- for semiconducting two-dimensional materials by the
- field effect transistor (FET) method.

For semiconducting two-dimensional materials, the carrier concentration is evaluated using a field effect transistor (FET) test by a measurement of the voltage shift obtained from transfer curve upon doping process. The FET test structure consists of three terminals of source, drain, and gate where voltage is applied to induce the transistor action. Transfer curves are obtained by measuring drain current while applying varied gate voltage and constant drain voltage with respect to the source which is grounded.

- The method is applicable to semiconducting two-dimensional materials with a bandgap like that in transition metal dichalcogenides (MoS_2 , MoTe_2 , WS_2 , WSe_2 , etc.) and black phosphorous. Pristine graphene shows semi-metallic characteristics without bandgap, and therefore this method is not applicable to pristine graphene. However, it can be used for other graphenes with bandgap (for example, semiconducting graphene oxide).
- It is likely that the measurement results will help to qualify technologies if they are usable for future electrical sub-systems or electronic device applications.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

There are no normative references in this document.